

Amendments to the Claims

1. (Canceled)

2. (Canceled)

3. (Canceled)

4. (Canceled)

5. (Canceled)

6. (Canceled)

7. (Canceled)

8. (Canceled)

9. (Canceled)

10. (Canceled)

11. (Canceled)

12. (Canceled)

13. (Canceled)

14. (Previously Presented) An adjustable bandwidth high pass filter, comprising:

a capacitor coupled between an input and a first output;

a first resistor having a first terminal and a second terminal, said first terminal coupled to said first output;

a second resistor having a third terminal and a fourth terminal, said third terminal coupled to said second terminal at a second output, said fourth terminal coupled to a ground;

a third resistor having a fifth terminal and a sixth terminal, said fifth terminal coupled to said first output; and

a switch coupled between said fourth terminal and said sixth terminal in a manner so that a ratio of a first voltage at said first output to a second voltage at said second output remains substantially independent of a position of said switch.

15. (Previously Presented) The adjustable bandwidth high pass filter of claim 14, wherein said switch is a semiconductor device.

16. (Previously Presented) The adjustable bandwidth high pass filter of claim 15, wherein said semiconductor device is a transistor.

17. (Previously Presented) The adjustable bandwidth high pass filter of claim 16, wherein said transistor is a metal oxide semiconductor field effect transistor.

18. (Previously Presented) The adjustable bandwidth high pass filter of claim 17, wherein said metal oxide semiconductor field effect transistor is a p-channel metal oxide semiconductor field effect transistor.

19. (Previously Presented) The adjustable bandwidth high pass filter of claim 18, wherein a gate terminal of said p-channel metal oxide semiconductor field effect transistor is coupled to a power supply voltage.

20. (Previously Presented) The adjustable bandwidth high pass filter of claim 19, further comprising a fourth resistor coupled to said gate terminal.

21. (Previously Presented) The adjustable bandwidth high pass filter of claim 19, further comprising a second semiconductor device coupled to said gate terminal.

22. (Previously Presented) The adjustable bandwidth high pass filter of claim 21, wherein said second semiconductor device is a triode device.

23. (Previously Presented) The adjustable bandwidth high pass filter of claim 19, further comprising a tri-state buffer coupled to said gate terminal.

24. (Previously Presented) The adjustable bandwidth high pass filter of claim 23, wherein said tri-state buffer couples said gate terminal to a ground when said tri-state buffer is in an on state.

25. (Previously Presented) The adjustable bandwidth high pass filter of claim 23, further comprising a second capacitor coupled between said gate terminal and a source terminal of said p-channel metal oxide semiconductor field effect transistor.

26. (Previously Presented) The adjustable bandwidth high pass filter of claim 25, further comprising a triode device coupled to said gate terminal.

27. (Previously Presented) The adjustable bandwidth high pass filter of claim 26, wherein said tri-state buffer and said triode device each have a large impedance.

28. (Previously Presented) The adjustable bandwidth high pass filter of claim 27, wherein a first voltage at said source terminal substantially equals a second voltage at said gate terminal when said p-channel metal oxide semiconductor field effect transistor is in an off state.

29. (Previously Presented) The adjustable bandwidth high pass filter of claim 14, further comprising a bias voltage coupled between said fourth terminal and said ground.

30. (Previously Presented) The adjustable bandwidth high pass filter of claim 14, wherein said switch is coupled between said third terminal and said sixth terminal.

31. (Previously Presented) The adjustable bandwidth high pass filter of claim 30, further comprising a bias voltage coupled between said fourth terminal and said ground.

32. (Currently Amended) An adjustable bandwidth high pass filter, comprising:

a first capacitor coupled between an input and an output;

a first resistor having a first terminal and a second terminal, said first terminal coupled to said output;

a second resistor having a third terminal and a fourth terminal, said third terminal coupled to said output, said fourth terminal coupled to a ground;

a metal oxide semiconductor field effect transistor coupled between said second terminal and said fourth terminal;~~and~~

a tri-state buffer coupled to a gate terminal of said metal oxide semiconductor field effect ~~transistor~~; transistor; and

a second capacitor coupled between said fourth terminal and said gate terminal.

33. (Currently Amended) An adjustable bandwidth high pass filter, comprising:

a first capacitor coupled between an input and an output;

a first resistor having a first terminal and a second terminal, said first terminal coupled to said output;

a second resistor having a third terminal and a fourth terminal, said third terminal coupled to said output, said fourth terminal coupled to a ground;

a metal oxide semiconductor field effect transistor coupled between said second terminal and said fourth terminal;~~and~~

a triode device coupled to a gate terminal of said metal oxide semiconductor field
effect ~~transistor~~; transistor; and
a second capacitor coupled between said fourth terminal and said gate terminal.

This listing of claims will replace all prior versions, and listings of claims in the
application.